In the Claims

1. (previously presented) A method of indicating reception performance of a wireless signal at a radio frequency peripheral component card of a computer system, said method comprising:

receiving said wireless signal at a wireless receiver of said radio frequency peripheral component card;

demodulating said wireless signal;

determining an error rate of a digital data portion of said wireless signal; and

indicating a quality level of reception of said wireless signal at said radio frequency peripheral component card based on said error rate using an indicator component of said radio frequency peripheral component card.

2. (original) The method recited in Claim 1 wherein said quality level of reception is indicated by a light emitting device.

3. (previously presented) The method recited in Claim 1 further comprising:

linearly translating said error rate into said quality level.

4. (original) The method recited in Claim 1 wherein said error rate is a packet error rate.

5. (original) The method recited in Claim 4 wherein said packet error rate is determined by a cyclic redundancy code (CRC) algorithm.

6. (previously presented) The method recited in Claim 4 wherein said packet error rate is determined by a forward error correction (FEC) algorithm.

7. (original) The method recited in claim 1 wherein said quality level is linearly proportional to said error rate of said wireless signal.

8. (previously presented) The method recited in Claim 1 further comprising:

adaptively updating said determining said error rate and said indicating said quality level.

9. (previously presented) The method recited in Claim 1 further comprising:

recording a history of said quality level with respect to another variable;

identifying a maximum quality level; and

indicating when said quality level is at said maximum level.

10. (previously presented) The method recited in Claim 1 further comprising:

providing feedback to control reception, said feedback related to said quality level of reception; and

adjusting said reception based on said feedback, thereby improving said quality level of said reception.

11. (previously presented) A radio frequency peripheral component card of a computer system for receiving a wireless signal, said radio frequency peripheral component card comprising:

a receiver;

an indicator coupled to said receiver;

a processor, said processor coupled to said receiver; and

a computer readable memory unit, said computer readable memory unit coupled to said processor, said computer readable memory unit containing program instructions stored therein that cause the processor to determine an error rate of a digital data portion of a received and demodulated wireless signal and to indicate with said indicator a quality level of reception of said wireless signal at said radio frequency peripheral component card based on said error rate.

12. (previously presented) The radio frequency peripheral component card recited in Claim 11 wherein said quality level of reception is indicated via a light emitting device.

13. (previously presented) The radio frequency peripheral component card recited in Claim 11 wherein said quality level is a linear translation of said error rate.

14. (previously presented) The radio frequency peripheral component card recited in Claim 11 wherein said error rate is a packet error rate.

15. (previously presented) The radio frequency peripheral component card recited in Claim 14 wherein said packet error rate is determined by a cyclic redundancy code (CRC) algorithm.

16. (previously presented) The radio frequency peripheral component card recited in Claim 14 wherein said packet error rate is determined by a forward error correction (FEC) algorithm.

17. (previously presented) The radio frequency peripheral component card recited in Claim 11 wherein said quality level is linearly proportional to said error rate of said wireless signal

18. (previously presented) The radio frequency peripheral component card recited in Claim 11 herein said program instruction further cause the processor to update, adaptively, said error rate and said quality level.

19. (previously presented) The radio frequency peripheral component card recited in Claim 11 wherein said program instruction further cause the processor to record a history of said quality level with respect to another variable, identify a maximum quality level, and indicate when said quality level is at said maximum level.

20. (previously presented) The radio frequency peripheral component card recited in Claim 11 wherein said program instruction further cause the processor to provide feedback to control reception, said feedback related to said quality level of reception.

21. (previously presented) A computer readable medium containing therein computer readable codes for causing a radio frequency peripheral component card of a computer system to implement a method of managing multipath signals, said method comprising:

receiving a wireless signal at a wireless receiver of said radio frequency peripheral component card;

demodulating said wireless signal;

determining an error rate of a digital data portion of said wireless signal; and

indicating a quality level of reception of said wireless signal at said radio frequency peripheral component card based on said error rate using and indicator component of said radio frequency peripheral component card.

22. (original) The computer readable medium recited in Claim 21 wherein said quality level of reception is indicated via a light emitting device.

23. (previously presented) The computer readable medium recited in Claim 21 wherein said method further comprises:

linearly translating said error rate into said quality level.

24. (original) The computer readable medium recited in Claim 21 wherein said error rate is a packet error rate.

25. (original) The computer readable medium recited in Claim 24 wherein said packet error rate is determined by a cyclic redundancy code (CRC) algorithm.

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26. (previously presented) The computer readable medium recited in Claim 21 wherein said packet error rate is determined by a forward error correction (F'EC) algorithm.

27. (original) The computer readable medium recited in Claim 21 wherein said quality level is linearly proportional to said error rate of said wireless signal.

28. (previously presented) The computer readable medium recited in Claim 21 wherein said method further comprises:

adaptively updating said determining said error rate and said indicating said quality level.

29. (previously presented) The computer readable medium recited in Claim 21 wherein said method further comprises:

recording a history of said quality level with respect to another variable;

identifying a maximum quality level; and

indicating when said quality level is at said maximum level.

30. (previously presented) The computer readable medium recited in Claim 21 wherein said method further comprises:

providing feedback to control reception, said feedback related to said quality level of reception; and

adjusting said reception based on said feedback, thereby improving said quality level of said reception.

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